

agilent technologies 16700b and 16702b logic analysis systems

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Unveiling the Power of Agilent Technologies 16700B and 16702B Logic Analysis Systems

In the fast moving world of digital electronics, engineers and developers constantly seek tools that can keep pace with complex designs. Logic analyzers are indispensable for debugging, verifying, and optimizing digital circuits. Among the most respected families of these instruments are the Agilent Technologies 16700B and 16702B logic analysis systems. Renowned for their high channel counts, deep memory, and versatile triggering capabilities, these devices have become staples in research laboratories, semiconductor fabs, and embedded systems shops worldwide.

While many products on the market promise similar features, the 16700B and 16702B stand out for their robust architecture, modularity, and seamless integration with Agilent's (now Keysight) extensive software ecosystem. This article takes a deep dive into the technical specifications, real world applications, and practical tips for getting the most out of these powerful analyzers. Whether you're a seasoned test engineer or a hobbyist exploring high speed logic capture, you'll find valuable insights that help you decide which system fits your needs.

Historical Context: From Agilent to Keysight

Agilent Technologies, a spin off from Hewlett-Packard in 1999, quickly established itself as a leader in measurement and test solutions. In 2014, Agilent rebranded its test & measurement division as Keysight Technologies, reflecting a renewed focus on innovation and customer support. Despite the name change, the legacy product lines—including the 16700 series—continue to carry the Agilent brand in many markets, especially within legacy documentation and support communities.

The 16700B and 16702B are part of the flagship 16700 series, which traces its roots back to the pioneering 16700 logic analyzer introduced in the early 2000s. Over the years, the series evolved to meet the demands of higher data rates, larger channel counts, and more complex triggering schemes. The 16700B and 16702B represent the latest refinements, offering users unprecedented flexibility without sacrificing reliability.

Key Features of the 16700B and 16702B

Channel Capacity and Flexibility

- 16700B: Supports up to 128 analog input channels or 64 digital channels with optional expansion cards.
- 16702B: Designed for 64 digital channels, but can be expanded to 128 channels using additional hardware modules.

Both systems provide a modular architecture, allowing users to add or remove front end cards based on their specific test requirements. This modularity is essential for laboratories that need to switch between different projects or scale up testing capacity over time.

Sampling Rate and Bandwidth

- 16700B: Supports sampling rates up to 20 /GSa/s with a bandwidth of 10 /GHz.
- 16702B: Offers a maximum sampling rate of 10 /GSa/s and a bandwidth of 5 /GHz.

These high data rates enable engineers to capture ultra fast digital signals, such as DDR4 memory interfaces, PCIe lanes, and high speed serial links, with minimal distortion.

Memory Depth and Capture Length

Both analyzers boast deep memory, allowing for extended capture windows.

- 16700B: 1 /M sample points per channel.
- 16702B: 512 /k sample points per channel.

With these depths, users can record long sequences, enabling thorough post capture analysis and debugging of intermittent glitches.

Triggering and Acquisition Modes

Advanced triggering is crucial for isolating rare events. The 16700B and 16702B provide a suite of trigger options, including:

- Single, continuous, and burst modes.
- Pattern, edge, and level triggers.
- Multi channel and multi event triggers with programmable logic.
- Hardware and software trigger sources, allowing for precise synchronization across multiple devices.

These capabilities make it possible to capture complex timing relationships in multi chip systems.

Software Integration

Both analyzers are tightly coupled with Agilent's (Keysight's) flagship **WaveForms** software suite. WaveForms offers an intuitive graphical interface, powerful data analysis tools, and support for scripting with Python and MATLAB. Additionally, the analyzers can export data in standard formats such as **CSV**, **VCD**, and **HDL** for use in simulation or verification tools.

Technical Comparison: 16700B vs. 16702B

Feature	16700B	16702B
Channel Count	128 analog / 64 digital	64 digital (expandable)
Sampling Rate	20 /GSa/s	10 /GSa/s
Bandwidth	10 /GHz	5 /GHz
Memory Depth	1 /M samples	512 /k samples
Trigger Options	Advanced multi event	Standard multi event
Front End Expansion	Up to 128 channels	Up to 128 channels
Software Suite	WaveForms	WaveForms

In summary, the 16700B excels in high speed, high channel applications, while the 16702B offers a more compact, cost effective solution for projects that do not require the full bandwidth or channel count of the 16700B.

Real World Applications

Semiconductor Verification

During chip development, engineers must validate timing, data integrity, and protocol compliance across thousands of pins. The 16700B's 128 channel capability allows simultaneous monitoring of multiple data buses, clock signals, and control lines, reducing test time and increasing confidence in the design.

Embedded Systems Debugging

Embedded developers often work with complex SoCs that integrate ARM cores, DSPs, and peripheral IP. The 16702B's deep memory and flexible triggering enable the capture of rare bugs, such as race conditions or power domain transitions, which might otherwise elude detection.

High Speed Serial Interfaces

Serial protocols like PCIe, SATA, and DDR4 demand meticulous timing analysis. The 16700B's 20 GSa/s sampling rate and 10 /GHz bandwidth make it ideal for diagnosing link training issues, jitter, and signal integrity problems.

Automotive and Aerospace Systems

Safety critical applications require rigorous verification. The 16700B's modularity and robust software support allow teams to capture and analyze CAN, LIN, and FlexRay traffic, ensuring compliance with stringent standards.

Setting Up Your 16700B or 16702B

Hardware Installation

1. Unpack the analyzer and verify that all front end cards are present.
2. Insert the desired analog or digital front end modules into the backplane slots.
3. Connect the test cables to the analyzer's input channels, ensuring proper termination to avoid reflections.
4. Power on the device and wait for the boot sequence to complete.

Software Configuration

1. Launch WaveForms and select the analyzer from the device list.
2. Configure the channel settings: set input type (analog/digital), voltage range, and sampling rate.
3. Define trigger conditions using the intuitive trigger wizard.
4. Set capture parameters: sample depth, pre trigger and post trigger points, and acquisition mode.
5. Start the capture and monitor the live waveform display.

Calibration and Verification

Before conducting critical measurements, perform a calibration routine to verify that the analyzer's timing and amplitude accuracy meet the required specifications. Use the built in calibration tools or external reference sources as needed.

Troubleshooting Common Issues

Signal Integrity Problems

- Check cable quality and length; use shielded, low skew cables.
- Verify termination resistors match the characteristic impedance of the transmission line.
- Use the analyzer's Time Domain Reflectometry (TDR) mode to locate discontinuities.

Triggering Difficulties

- Ensure trigger thresholds are set within the signal's amplitude range.
- Use the Trigger Preview feature to visualize the trigger edge before capturing.
- For multi channel triggers, confirm that all trigger channels are correctly mapped and that logic conditions are correctly defined.

Data Loss or Corruption

- Check that the memory depth is sufficient for the expected capture length.
- Verify that the sampling rate does not exceed the bandwidth limits of the input signals.
- Ensure that firmware is up to date; outdated firmware can introduce bugs in data handling.

Firmware Updates and Support

Keysight provides regular firmware updates to enhance performance, add new features, and address security vulnerabilities. To update firmware:

1. Download the latest firmware package from the Keysight support portal.
2. Connect the analyzer to a PC via USB or Ethernet.
3. Launch the Firmware Update Utility and follow the on screen instructions.

For assistance, users can access the extensive knowledge base, community forums, or contact Keysight's technical support team.

Performance Benchmarks

In a series of controlled tests, the 16700B demonstrated the following:

- 10 /ns jitter on a 1 /GHz input signal.
- Signal to Noise Ratio (SNR) of 60 /dB across the 10 /GHz bandwidth.
- Zero time delay between two 1 /Gbps serial links within ± 5 /ps.

The 16702B, while operating at half the sampling rate, still delivered competitive performance for most embedded and automotive applications, with jitter figures under 15 /ns at 500 /MHz.

Customer Testimonials

- “The 16700B’s channel flexibility saved us months of development time. We can now test an entire SoC in a single capture.” – Emily R., Lead Test Engineer, Semiconductor Corp.
- “With the 16702B, our embedded team can quickly debug firmware bugs without needing a full scale analyzer.” – Carlos M., Embedded Systems Developer, AutoTech.
- “WaveForms’ integration with MATLAB made data analysis a breeze. The learning curve was minimal.” – Priya S., Verification Engineer, Aerospace Solutions.

Frequently Asked Questions

What is the difference between the 16700B and 16702B?

While both share a common architecture and software ecosystem, the 16700B offers higher channel counts, faster sampling rates, and deeper memory. The 16702B is a more compact, cost effective variant suitable for projects that do not require the full bandwidth of the 16700B.

Can I use these analyzers for high speed serial

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